

**Provisional List of the Eligible/Ineligible candidate for written test/Interview in the
Department of ELECTRONICS AND COMMUNICATION ENGINEERING for Ph.D. Admission ODD Semester 2026-27**

Sr No	Form No	Applicant Name	Department	Admission Category	Admission Sub Category	Eligible/ Ineligible	Reason for Ineligible	Entrance written Test waived off (Yes or No) if yes give reason (Eligible for Direct Interview)	Remarks
1	PHD/2026/5011	CHANDAN KUMAR	Electronics & Comm. Engineering-ECE	Full Time	Full Time with Institute Assistantship (with valid GATE/UGC-NET/CSIR-NET/National Level Exam) or (Masters degree with valid GATE Score)	Eligible		Yes	
2	PHD/2026/5064	KHUSHI CHHABRA	Electronics & Comm. Engineering-ECE	Full Time	Full Time with Institute Assistantship (with valid GATE/UGC-NET/CSIR-NET/National Level Exam) or (Masters degree with valid GATE Score)	Eligible		Yes	Direct PhD after Btech (from MNIT with 8.4 CGPA), valid GATE score, about 2 years work experience in Qualcomm and has one conference publication.
3	PHD/2026/5215	MEGHA MITTAL	Electronics & Comm. Engineering-ECE	Full Time	Full Time with Institute Assistantship (with valid GATE/UGC-NET/CSIR-NET/National Level Exam) or (Masters degree with valid GATE Score)	Eligible		Yes	
4	PHD/2026/5298	DIVANSHU JAIN	Electronics & Comm. Engineering-ECE	Part Time	Part Time (candidate working within 70 km of Jaipur)	Eligible		No	NOC is mandatory at the time of interview.
5	PHD/2026/5523	PUNEET KUMAR	Electronics & Comm. Engineering-ECE	Full Time	Full Time with Institute Assistantship (with valid GATE/UGC-NET/CSIR-NET/National Level Exam) or (Masters degree with valid GATE Score)	Ineligible	Direct PhD after Btech (< 75%)		
6	PHD/2026/5695	AASHISH KUMAR SHARMA	Electronics & Comm. Engineering-ECE	Part Time	Part Time (candidate working within 70 km of Jaipur)	Eligible		No	NOC is mandatory at the time of interview.
7	PHD/2026/5812	HEMA SINGH	Electronics & Comm. Engineering-ECE	Part Time	Part Time (candidate working within 70 km of Jaipur)	Eligible		No	NOC is mandatory at the time of interview.
8	PHD/2026/6044	VIPRA BOHARA	Electronics & Comm. Engineering-ECE	Part Time	Part Time (candidate working within 70 km of Jaipur)	Eligible		No	NOC is mandatory at the time of interview.
9	PHD/2026/6061	BHUDEV TYAGI	Electronics & Comm. Engineering-ECE	Full Time	Full Time with Institute Assistantship (with valid GATE/UGC-NET/CSIR-NET/National Level Exam) or (Masters degree with valid GATE Score)	Ineligible	No GATE score	No	
10	PHD/2026/6149	MANISHA KUMAWAT	Electronics & Comm. Engineering-ECE	Part Time	Part Time (candidate working within 70 km of Jaipur)	Ineligible	The applicant is currently not employed.		
11	PHD/2026/6379	SAURABH GUPTA	Electronics & Comm. Engineering-ECE	Full Time	Full Time with Institute Assistantship (with valid GATE/UGC-NET/CSIR-NET/National Level Exam) or (Masters degree with valid GATE Score)	Eligible		Yes	
12	PHD/2026/5462	SHIVANI DWIVEDI	Electronics & Comm. Engineering-Embedded Systems	Full Time	Full Time (DST Inspire)	Eligible		No	
13	PHD/2026/5056	EESAM SAI TEJA	Electronics & Comm. Engineering-VLSI Design	Full Time	Full Time with Institute Assistantship (with valid GATE/UGC-NET/CSIR-NET/National Level Exam) or (Masters degree with valid GATE Score)	Ineligible	Result awaited		
14	PHD/2026/5575	DEEPAK GUDIVADA	Electronics & Comm. Engineering-VLSI Design	Full Time	Full Time with Institute Assistantship (with valid GATE/UGC-NET/CSIR-NET/National Level Exam) or (Masters degree with valid GATE Score)	Eligible		Yes	
15	PHD/2026/5806	ALKA RANI	Electronics & Comm. Engineering-VLSI Design	Part Time	Part Time (candidate working within 70 km of Jaipur)	Eligible		No	NOC is mandatory at the time of interview.
16	PHD/2026/6415	ANKIT GOEL	Electronics & Comm. Engineering-VLSI Design	Full Time	Full Time with Institute Assistantship (with valid GATE/UGC-NET/CSIR-NET/National Level Exam) or (Masters degree with valid GATE Score)	Eligible		Yes	
17	PHD/2026/6484	RASHMI SHRIVASTAVA	Electronics & Comm. Engineering-VLSI Design	Full Time	Full Time with Self-Financed (without any scholarship / assistantship)	Eligible		No	
18	PHD/2026/6518	SHIVAM CHAUDHARY	Electronics & Comm. Engineering-VLSI Design	Full Time	Full Time with Institute Assistantship (with valid GATE/UGC-NET/CSIR-NET/National Level Exam) or (Masters degree with valid GATE Score)	Eligible		Yes	
19	PHD/2026/5201	TOUFEEQ AHMED	Electronics & Comm. Engineering-Wireless and Optical Communication	Full Time	Full Time with Institute Assistantship (with valid GATE/UGC-NET/CSIR-NET/National Level Exam) or (Masters degree with valid GATE Score)	Ineligible	Result awaited		
20	PHD/2026/6258	MANJU CHOUHARY	Electronics & Comm. Engineering-Wireless and Optical Communication	Full Time	Full Time with Institute Assistantship (with valid GATE/UGC-NET/CSIR-NET/National Level Exam) or (Masters degree with valid GATE Score)	Ineligible	Result awaited		