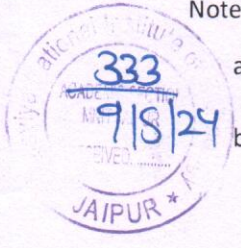**Proposal for announcing seat under the Institute Internship Program**

(separate form to be filled for seat under Institute funding and project funding)

1. Name of faculty member proposing: Dr. Bharat Choudhary
2. Department/Centre    Electronics & Communication Engineering
3. Topic on which work is proposed: Analog & Digital VLSI Design

4. Preferred period of internship (after May 20th): Between 01st Jun 2024 to 31st Jul 2024

5. Qualification of student (branch/semester of study): Electronics & Communication
   (II Year / III Year UG Student)

6. Brief description of work (300-500 words): I am looking for research interns who are willing to work in the field of VLSI Design, Device modeling & Simulation.

7. Expected learning of student (upto 100 words): Student will learn skills related to VLSI Design and will experience of various EDA tools available to our department.

8. Nature of work: (Experimental/simulation/mathematical modelling/data collection-analysis etc.): upto 50 words
   Simulation, data collection & analysis

9. If the seat is under project sponsored category: Yes/No ✓
   a) If yes, number of seats announced:      Nil
   b) Name and ID no. of project from which stipend is chargeable   Nil


Bharat
08·05·2024
Signature of faculty member

Name of department/Centre
Electronics & Communication

296/ECE
8/5/24

DR (Academic)

Note:

a) Proposing faculty member needs to be available at the Institute during the period internship is offered

b) No extra space or funding than the stipend will be provided by the institute for this purpose