

**Provisional List of the Eligible/Ineligible candidate for written test/Interview in the  
Department of ELECTRONICS AND COMMUNICATION ENGG. for Ph.D. Admission ODD Semester 2024-25**

Sr No	Form No	Applicant Name	Specialization	Eligible/ Ineligible	Reason for Ineligible	Entrance written Test waived off (Yes or No) if yes give reason	Remarks
1	PHD/2024/2024006	LAXMI NARAYAN BALAI	Electronics & Comm. Engineering-ECE	Ineligible		No	1. No GATE score
2	PHD/2024/2024109	PURANIK PARAG VINOD	Electronics & Comm. Engineering-ECE	Ineligible	Less than 2 years of experience in the current organization	NA	
3	PHD/2024/2024359	MOHIT SHARMA	Electronics & Comm. Engineering-ECE	Eligible		No	
4	PHD/2024/2024514	ABHISHEK KULSHRESTHA	Electronics & Comm. Engineering-ECE	Ineligible		No	1. No GATE score
5	PHD/2024/2024613	SUNIL PATEL	Electronics & Comm. Engineering-ECE	Eligible		No	
6	PHD/2024/2024724	RISHIKA POSWAL	Electronics & Comm. Engineering-ECE	Ineligible		No	1. No GATE score
7	PHD/2024/2024790	MRADURAJE SISODIYA	Electronics & Comm. Engineering-ECE	Ineligible		No	1. No GATE score mentioned in the form
8	PHD/2024/2024896	DEEPMALA KULSHRESHTH	Electronics & Comm. Engineering-ECE	Eligible		No	With GATE
9	PHD/2024/2024963	ASHISH KULSHRESTHA	Electronics & Comm. Engineering-ECE	Eligible		No	
10	PHD/2024/2025083	DEEPMALA KULSHRESHTH	Electronics & Comm. Engineering-ECE	Eligible		No	With GATE
11	PHD/2024/2025162	JYOTI MAHAWAR	Electronics & Comm. Engineering-ECE	Eligible		No	Experience < 1 year in current organization but total experience is more than 2 years
12	PHD/2024/2025323	ASHOK KUMAR AWASTHI	Electronics & Comm. Engineering-ECE	Ineligible	Executive/Professional category: No research publication	NA	
13	PHD/2024/2024380	ANJALI CHOKRAYAT	Electronics & Comm. Engineering- Embedded Systems	Eligible		No	
14	PHD/2024/2024100	LAXMI	Electronics & Comm. Engineering- VLSI Design	Eligible		No	
15	PHD/2024/2024699	DINESH SINGH	Electronics & Comm. Engineering- VLSI Design	Eligible		No	With GATE
16	PHD/2024/2025034	ASHISH CHAUHAN	Electronics & Comm. Engineering- VLSI Design	Ineligible	Executive/Professional category: Insufficient experience (<12 years) for this category	NA	
17	PHD/2024/2025035	VIPRA BOHARA	Electronics & Comm. Engineering- VLSI Design	Ineligible		No	1. No Gate score mentioned in the form
18	PHD/2024/2025147	HIMANSHU KUMAR	Electronics & Comm. Engineering- VLSI Design	Eligible		No	
19	PHD/2024/2025166	SHISHANK SAHU	Electronics & Comm. Engineering- VLSI Design	Eligible		No	

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20	PHD/2024/2025237	KUSHAGRA SHARMA	Electronics & Comm. Engineering- VLSI Design	Ineligible	Mtech in Material Sciences	NA	As per recommendation of Committee
21	PHD/2024/2025459	KESHAV SHARMA	Electronics & Comm. Engineering- VLSI Design	Ineligible	Executive/Professional category: Insufficient experience (<12 years) for this category	NA	
22	PHD/2024/2024683	ARINDAM MUKHERJEE	Electronics & Comm. Engineering- Wireless and Optical Communication	Eligible		No	
23	PHD/2024/2024954	SANDEEP JWALA	Electronics & Comm. Engineering- Wireless and Optical Communication	Ineligible	Executive/Professional category: No clear Information about employment and no information about research publications.	NA	No publication mentioned. Wrong information about experience in form
24	PHD/2024/2025234	TANISH DAGAR	Electronics & Comm. Engineering- Wireless and Optical Communication	Provisionally Eligible		No	Candidate has claimed 63% in PG. To be checked at the time of interview.

**Above candidates are provisionally eligible based on information provided in the application form in PDF format. Their candidature is subject to the fulfillment of eligibility criteria at the time of document verification during interview.**