Organized by Electronics & ICT Academy



MNIT Jaipur http://www.mnit.ac.in/eict

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Preamble (Electronics & ICT Academy)

Government of India had announced a National Policy on Skill Development, which has set a target of skilling 500 million people by 2022 in the domain of Electronics & IT. Under the plan scheme of "Digital India Manpower Development". MeitY has set up seven (07) Electronics and ICT Academies as a unit in 03 IITs, 03 NITs and 01 IIIT with an objective of faculty/mentor development/up gradation in the areas related to Electronics & ICT leading ultimately to improved employability of graduates/diploma holders. MNIT Jaipur has set up such an academy for providing specialized training to faculty and industry persons in the states/UTs of Rajasthan, Gujarat, Daman & Diu, Dadra Nagar Haveli.

(A) Issues-

- IT Hardware and Electronics Manufacturing industry- availability of properly trained, skilled and qualified manpower
- 2. Number of quality PhDs generated in IT / Computer Science is very low
- In E & ICT domain- there is a very high degree of obsolescence of existing technologies and faster emergence of newer technologies
- (B) Approach-
- A focused faculty training/updation programme for IT, Electronics and related sectors
- 2. Spreading up and continuous updation regarding Emerging Technology
- 3. Training and consultancy services for Industry
- Design, Develop and Deliver specialized modules for specific research areas and Industry
- 5. Providing advice and support for technical incubation and entrepreneurial activities

Advanced Embedded Linux and Device Driver Development

June 24 - 28, 2019

Venue: C S Patel Institute of Technology CHARUSAT, Changa, Gujarat, India

Faculty Development Programme Sponsored by



Department of Electronics & Information Technology Ministry of Communications & Information Technology Government of India

Ministry of Electronics & Information Technology Government of India meity.gov.in/content/schemes-projects

http://academymnit.wordpress.com

The five days workshop on advanced embedded Linux and device driver development is organized for faculty members of science, engineering & technological institutions and persons from industry. During these five days, participants will learn the frame work of Linux device driver and architecture of ARM processor. Participant will be ready to develop industrial project based on embedded Linux, able to write custom device driver for ARM Linux. If a participant attends on all 5 days, one week FDP certificate is provided.

Programme Topics:

- Operating system fundamentals and Hands on Linux operating system
- Kernel Module programing and Linux Device driver development
- Boot loader configuration and development on PhyBoard-Wega ARM Board
- GPIO Driver development on ARM Processor
- MMC Domain in AM335x board and its implementation
- Hardware Access and Interrupt handling in ARM Processor

Invited Experts:

Experienced Industry Experts from TechVeda Hyderabad, AES Lab, Bangalore and e-Infochips, Ahmeadabad

Program Coordinators:

Dr. Hiren Mewada	hirenmewada.ec@charusat.ac.in	9725019975 (M)
Prof. Jignesh Patoliya	jigneshpatoliya@charusat.ac.in	9727706794 (M)
Dr. Amit Joshi	amjoshi.ece@mnit.ac.in	9549655371 (M)

Registration:

Registration is open to faculty, industry persons, doctoral and postgraduate students of programmmes related to Electronics and Comm. Engg, Computer Science and Electrical Engineering.

Participants will be admitted on first-come first-served basis.

Register on line at - http://www.mnit.ac.in/eict/acad_training_prg.php Fee:

(A) The one-time registration fee of Rs. 500/- is to be paid by each participant attending first time, irrespective of affiliation. This fee is not applicable for those participants, who have attended any academy training programme earlier.

(B) (i) The participants from academia and research scholars are required to pay a further fee of Rs. 2000/- (faculty/research-scholars). Rest expenditure is sponsored by MeitY through Electronics & ICT Academy at MNIT Jaipur.

(ii) The participants from industries, UG/PG students are required to pay a further fee of Rs. 5000/-.

(iii) Applicable relaxation for SC/ST candidate is 50%.

(iv) Fee once paid will not be refunded back, however, it may be adjusted to another FDP.

(C) The fee covers the participation in the programme, registration material including tutorial notes, boarding (breakfast/lunch) on all the days of the workshop. The travel and other expenses would have to be borne by the participants or their parent-organizations.

(D) Accommodation for a very limited number of participants in Hostel/Guest house of CSPIT campus can be arranged on first-come first-serve & additional payment basis.

(E) The organizers should receive the registration amount through online payment/NEFT/IMPS.

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	Account Name-	Account Number-		
	'Electronics and ICT Academy MNIT Jaipur'	676801700483		
	Bank address-	IFSC Code-		
	ICICI Bank, MNIT Campus Branch, Jaipur	ICIC0006768		

(F). Please pre-intimate your desire to participate and for accommodation to programme coordinator through e-mail, immediately after online registration. \rightarrow For any other query else then this FDP, email us at academy@mnit.ac.in