Organized by **Electronics & ICT Academy**



Intensive Workshop on Authoring Technical Documents with Latex (A) Technical Writing (B) Reviewing Manuscripts

Department of Electronics & Information Technology Ministry of Communications & Information Technology Government of India

Faculty Development Programme

Sponsored by

Electronics & Information Technology

meity.g ov.in/content/schemes-projects

5-7 April & 13-14 April, 2019 (weekends)

Venue: Prabha Bhawan, MNIT Jaipur

http://www.mnit.ac.in/eict

Chairman, Advisory Board, EICT Academy & **Director MNIT Jaipur** Prof. A. B. Gupta

Honorary Academic Chair, EICT Academy Prof. V. Sinha

Chief Investigator, EICT Academy Prof. Vineet Sahula, ECE

Co- Chief Investigators, EICT Academy Prof. L. Bhargava, ECE Dr. Pilli Emmanuel Shubhakar, CSE Dr. C. Periasamy, ECE Dr. S. J. Nanda, ECE Head, ECE (Prof. D. Boolchandani) Head, CSE (Prof. Girdhari Singh)

Preamble (Electronics & ICT Academy)

Government of India had announced a National Policy on Skill Development, which has set a target of skilling 500 million people by 2022 in the domain of Electronics & IT. Under the plan scheme of "Digital India Manpower Development". DeitY has set up seven (07) Electronics and ICT Academies as a unit in 03 IITs, 03 NITs and 01 IIIT with an objective of faculty/mentor development/up gradation in the areas related to Electronics & ICT leading ultimately to improved employability graduates/diploma holders. MNIT Jaipur has set up such an academy for providing specialized training to faculty and industry persons in the states/UTs of Rajasthan, Gujarat, Daman & Diu, Dadra Nagar Haveli.

(A) Issues-

- IT Hardware and Electronics Manufacturing industry- availability of properly trained, skilled and qualified manpower
- Number of quality PhDs generated in IT / Computer Science is very low
- In E & ICT domain- there is a very high degree of obsolescence of existing technologies and faster emergence of newer technologies

(B) Approach-

- A focused faculty training/updation programme for IT, Electronics and related sectors
- Spreading up and continuous updation regarding Emerging Technology
- Training and consultancy services for Industry
- Design, Develop and Deliver specialized modules for specific

Two independent workshops of total Five Days duration on Authoring Technical Documents with Latex is being organized for UG, PG students, Research Scholars, faculty members of engineering & technological institutions and persons from industry. Total 5 days intensive workshop is spread over two weekends for the two parts: Part A: Technical Report Writing from 5-7 April, 2019: Part B: Authoring & Reviewing of research manuscripts from 13-14 April, 2019. It will include hands-on lab sessions, and discussions/ presentation sessions. If a participant attends on all 5 days, one week FDP certificate is provided, else there would be separate certificate for each part.

Programme topics

Part A: Technical Report Writing from 5-7 April, 2019

- A. Documents Preparation using Latex starting with Installation of MikTex, Text Editor B: Figures, Table, Equation, Artwork in Latex
- C. Managing Bibliography(Mendeley, JabRef) in Latex, Presentation / Thesis writing in

Part B: Authoring & Reviewing of research manuscript 13-14 April, 2019

- A. Manuscript preparation, guidelines, grammar and spell checking
- B. Reviewing Technical articles, Responding to reviewer comments, Author kits

Invited Experts - Faculty from IITs/NITs

Programme coordinators

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Registration

Registration is open to faculty, industry persons, doctoral and UG/PG students of programmes related to Electronics, Communication, and Information technologies. Participants will be admitted on a first-come first-served basis. Selected participants will be notified on or before 2 April, 2018. Register on line at http://www.mnit.ac.in/eict

- (A) The one-time registration fee of Rs. 500/- is to be paid by each participant attending first time, irrespective of affiliation. This fee is not applicable for those participants, who have attended any academy training programme earlier.
- (B) (i) The participants from academia and UG/PG/research scholars are required to pay a further fee of Rs. 2000/- (faculty/research-scholars) for all 5 days FDP. To attend Part A workshop only, the registration fees is Rs. 1500/-, while for Part B, it is Rs. 1000/-
- (ii) The participants from industries, UG/PG students are required to pay a further fee of Rs.
- (iii) Applicable relaxation for SC/ST candidate is 50%.
- (iv) Fee once paid will not be refunded back, however, it may be adjusted to another FDP.
- (C) The fee covers the participation in the programme, registration material including tutorial notes, boarding (breakfast/lunch/dinner) on all the days of the workshop. The travel and other expenses would have to be borne by the participants or their parent-organizations.
- (D) Complimentary lodging for a very limited number of participants is available in Guest rooms of Aurobindo hostel. Also, accommodation in Guesthouse-2 can be arranged on firstcome first-serve & additional payment basis.
- The organizers should receive the online registration amount through payment/NEFT/IMPS.

Account Name-	Account Number-	
'Electronics & ICT Academy MNIT Jaipur'	676801700483	
Bank address-	IFSC Code-	
ICICI Bank, MNIT Campus Branch, Jaipur	ICIC0006768	

- (f) Please pre-intimate your desire to participate through e-mail, before registration form reaches us.
- → For any other query else then this FDP, email us at academy@mnit.ac.in