

Malaviya National Institute of Technology (MNIT) Jaipur
Department of Electrical Engineering

Advertisement for Junior Research Fellow (JRF)

Applications are invited for Junior Research Fellow (JRF) in the Department of Electrical Engineering, MNIT Jaipur for a research project entitled "**Design & Development Of Advanced Control Techniques For Multifunctional Inverter Based Grid-tied Solar Energy System**" sponsored by Department of Science and Technology (DST), Government of Rajasthan, Jaipur.

The selected candidates can opt for PhD program announced by MNIT (Twice a Year) in case they are interested in pursuing PhD at MNIT as per provision in the above work area.

No. of Position: One (1 number)

Consolidated Fellowship: Rs. 11,400/- per month for the first two years and Rs. 15,000/- per month for the third year.

Duration: Three years

Qualification:

- First Class with not less than 60% aggregate or not less than 6.5 CGPA on a 10 point scale in B.Tech/B.E. in Electrical Engineering/ Electrical & Electronics Engineering/ Power Electronics and First class with not less than 60% aggregate (or) not less than 6.5 CGPA on a 10 point scale in M.Tech/M.E. in Power Electronics & Drives/ Power System/Electrical Engineering/Power & Energy System/ Electrical Energy System/Machine & Drive or relevant specialization.
- Fresher and highly motivated, research oriented candidates are encouraged to apply.
- Relevant hands-on laboratory experience in the area of Power Electronics converter and their fabrication with good programming and software skills (MATLAB/Simulink, C).
- Research experience in the relevant area, supported by publications in high impact peer reviewed international journals would be a plus point.
- Good in English language (both written and oral) and presentation skills.

How to apply: The candidates who are interested to apply for the above post may send their application in prescribed form (format attached) with complete information and attachments (self attested, scanned copy of certificates/documents/degree/award etc.) along with latest CV to the undersigned in soft copy via email to nitingupta.ee@mnit.ac.in as a single pdf document before **10 September 2017**.

The application should also be sent in hard copy by speed/registered post at address given below, by marking the envelop, Application for “**Design & Development ...Energy System**” before **13 September 2017**.

Interview: Interview will be held tentatively in the mid of September, 2017 in the Electrical Engineering Department of MNIT Jaipur.

Candidates may also note the following:

- a) MNIT Jaipur reserves the right to fix suitable criteria for short listing of eligible candidates satisfying advertised qualification and requirement of the project post.
- b) The committee also reserves right for not selecting any candidate/offering lower post in case candidates are not found suitable for the applied post.
- c) Only shortlisted candidates will be informed for the interview by email/mobile and no separate interview letter will be issued for the same.
- d) No TA/DA will be provided to the candidates for attending the interview. Calling for interviewing is not a guarantee for selection.
- e) No communications will be entertained for being called/ not- called and/or selected/ not selected for the advertised position.
- f) Candidates who are working in Private/Govt. organization should produce NOC at the time of interview.
- g) **The selected candidate can opt for PhD program announced by MNIT (Twice a Year) in case he/she is interested in pursuing PhD at MNIT as per provision in the above work area.**

For any further information, the applicants may contact PI (Address for Postage):

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Assistant Professor

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